

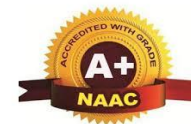


ADITYA COLLEGE OF ENGINEERING, MADANAPALLE

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

TIME TABLE

Academic Year :2025-2026 - II SEM



Year : III-II B. Tech –ECE-B

Room No.: 317

w.e.f: 24-11-2025

Day/Time	09:00 - 10:00	10:00 - 11:00	11:00 - 11:10	11:10 - 12:05	12:05 - 01:00	01:00 - 01:50	01:50 - 02:50	02:50 - 03:50	03:50 - 04:00	04:00 - 05:00
MON	/	VLSID/MW&OC Lab	B R E A K	VLSID/ MW&OC Lab		L U N C H B R E A K	DIP	DSP	B R E A K	VLSID
TUE		OS		MW&OC	ES&IoT		VLSID	DIP		TPW&IPR (KMB)
WED		ML&DSP		ML & DSP			ES&IoT	OS		MW&OC(T)
THU		DIP		OS	ES&IoT		VLSID	DSP		TPW&IPR (RT)
FRI		MW&OC		DSP	Mentoring/ Activity		VLSID/ MW&OC Lab			VLSID/MW&OC Lab
SAT		OS		VLSID	MW&OC		DIP	ES&IoT		DSP(T)

S.No.	Title of the Subject		Subject Code	Name of the faculty
1	DSP	Digital Signal Processing	23A04601	Dr. K. V. Vara Prasad
2	MW&OC	Microwave & Optical Communication	23A04602T	Mrs. S. Heena Kousar
3	VLSID	VLSI Design	23A04603T	Mr. C. Manoj Kumar
4	ES&IOT	Professional Elective-II : Embedded Systems & IOT	23A04604b	Dr. M. Jagadeesh Babu
5	DIP	Professional Elective-III: Digital Image processing	23A04605a	Dr. R. Triveni
6	OS	Open Elective - II : Operating Systems	23A32502T	Mrs. B. Vijitha
7	MW&OC Lab	Microwave & Optical Communication lab	23A04602P	Mrs. S. Heena Kousar/ Mrs. B. Vijitha
8	VLSID Lab	VLSI Design lab	23A04603P	Dr. J.Pradeep/Mr. N. RamaKumar
9	ML&DSP	Skill Enhancement course: Machine Learning & DSP	23A04607	Mr. M. Karunakar Reddy/Mr. B. R. Darshan
10	TPW&IPR	Technical Paper Writing & IPR	23A52601	Dr. R. Triveni / Mr. K. Manju Balaji
Class In-charge:			Mrs. B. Vijitha	

Department Timetables Officer

Head of the Department
 ELECTRONICS & COMMUNICATION ENGINEERING
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Principal
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 Madanapalle - 517325