



# ADITYA COLLEGE OF ENGINEERING, MADANAPALLE

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

## TIME TABLE

Academic Year :2025-2026 - II SEM



Year : III-II B. Tech –ECE-A

Room No.: 316

w.e.f: 24-11-2025

Day/Time	09:00 - 10:00	10:00 - 11:00	11:00 - 11:10	11:10 - 12:05	12:05 - 01:00	01:00 - 01:50	01:50 - 02:50	02:50 - 03:50	03:50 - 04:00	04:00 - 05:00
MON	/	VLSID	B R E A K	DIP	DSP	L U N C H B R E A K	OS	ES&IoT	B R E A K	MW&OC(T)
TUE		ES&IoT		OS	DSP		VLSID/ MW&OC Lab			VLSID/MW&OC Lab
WED		DIP		VLSID	MW&OC		TPW&IPR (RT)	DSP		ES&IoT
THU		MW&OC		VLSID	DIP		VLSID/ MW&OC Lab			VLSID/MW&OC Lab
FRI		ES&IoT		OS	Mentoring/ Activity		VLSID	DSP(T)		TPW&IPR (KMB)
SAT		ML&DSP		ML & DSP			OS	MW&OC		DIP

S.No.	Title of the Subject		Subject Code	Name of the faculty
1	DSP	Digital Signal Processing	23A04601	Dr. K. V. Vara Prasad
2	MW&OC	Microwave & Optical Communication	23A04602T	Mrs. S. Heena Kousar
3	VLSID	VLSI Design	23A04603T	Mr. C. Manoj Kumar
4	ES&IoT	<b>Professional Elective-II</b> : Embedded Systems & IOT	23A04604b	Dr. M. Jagadeesh Babu
5	DIP	<b>Professional Elective-III</b> : Digital Image processing	23A04605a	Dr. R. Triveni
6	OS	<b>Open Elective - II</b> : Operating Systems	23A32502T	Mrs. B. Vijitha
7	MW&OC Lab	Microwave & Optical Communication lab	23A04602P	Mrs. S. Heena Kousar/ Mrs. B. Vijitha
8	VLSID Lab	VLSI Design lab	23A04603P	Dr. J.Pradeep/Mr. N. RamaKumar
9	ML&DSP	<b>Skill Enhancement course</b> : Machine Learning & DSP	23A04607	Mr. M. Karunakar Reddy/Mr. B. R. Darshan
10	TPW&IPR	Technical Paper Writing & IPR	23A52601	Dr. R. Triveni
			<b>Class In-charge:</b>	Mrs. S. Heena Kousar

Department Timetables Officer

Head of the Department  
 Elect. Engg. Department  
 ADITYA COLLEGE OF ENGINEERING  
 MADANAPALLE - 517 325

Principal  
 Aditya College of Engineering  
 Madanapalle - 517325