



ADITYA COLLEGE OF ENGINEERING: MADANAPALLE

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

TIME TABLE

Academic Year :2024-25 II SEMESTER



Year & Sem : III-II B.Tech

Section: ECE-A

Room No.: 315

w.e.f: 11-12-2024

Day/Time	09:00 - 10:00	10:00 - 11:00	11:00 - 11:10	11:10 - 12:05	12:05 - 01:00	01:00 - 01:50	01:50 - 02:50	02:50 - 03:50	03:50 - 04:00	04:00 - 05:00
MON	X	←	B R E A K	VLSI (A1)/CN(A2) LAB →	L U N N C H B B R E A K	POS(T)	AME	B R E A K	VLSI	
TUE		ESD		IPR		POS	VLSI		CN	AME(T)
WED		←		CN (A1)/AME(A2) LAB →		VLSI(T)	ESD		ESD	AME
THU		POS		VLSI		CN	← RFSD		←	→
FRI		CN		RFSD		AME(A1)/VLSI(A2) Lab	←		←	→
SAT		VLSI		AME		ESD	CN		POS	IPR

S.No.	Title of the Subject		Subject Code	Name of the faculty
1	AME	Antennas & Microwave Engineering	20A04601T	Mrs.S.Heena Kousar
2	VLSI	VLSI Design	20A04602T	Mr. C. Manoj Kumar
3	CN	Communication Networks	20A04603T	Mr. K. Balachandra
4	ESD	Professional Elective Course-II :Embedded System Design	20A04604b	Dr. M. Jagadeesh Babu
5	POS	Open elective II-Principles of Operating Systems	20A05605a	Mrs. B. Vijitha
6	AME Lab	Antennas & Microwave Engineering Lab	20A04601P	Mr. A. Amareswara Kumar/Mrs.S.Heenakousar
7	VLSI Lab	VLSI Design Lab	20A04602P	Mr. C. Manoj Kumar/ Mr. N.Ramkumar
8	CN Lab	Communication Networks Lab	20A04603P	Mr. K. Balachandra/ Mr. B.R. Darshan
9	RFSD	Skill oriented course - IV :RF System Design	20A04607	Mr. M. Karunakar Reddy/Dr. M. Jagadeesh Babu
10	IPR	Mandatory Non-credit Course :Intellectual Property Rights & Patents	20A99601	Mr. K. Manju Balaji
Class In-charge:				Mrs. B. Vijitha

Department Head

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Head of the Department

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Electronics & Communication Engineering
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MADANAPALLE - 517 325

Principal
Aditya College of Engineering
Madanapalle - 517325

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