



ADITYA COLLEGE OF ENGINEERING: MADANAPALLE

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

TIME TABLE

Academic Year : 2024-2025 ODD SEM



Year : III-I B. Tech –ECE-A

Room No.: 313

w.e.f: 08-07-2024

Day/Time	09:00 - 10:00	10:00 - 11:00	11:00 - 11:10	11:10 - 12:05	12:05 - 01:00	01:00 - 01:50	01:50 - 02:50	02:50 - 03:50	03:50 - 04:00	04:00 - 05:00
MON	/	MPMC	B R E A K	CSE	JP	L U N C H B R E A K	DSP(T)	CAO	B R E A K	Activity
TUE		DSP		MPMC	JP		← MPMC LAB			→
WED		CAO		← PCB LAB →			JP(T)	DSP		ES
THU		←		DSP LAB →			MPMC	CAO		CSE(T)
FRI		MPMC		DSP	JP		CSE	CAO(T)		CSE
SAT		CAO		DSP	CSE		JP	MPMC(T)		PCB

S.No.	Title of the Subject		Subject Code	Name of the faculty
1	CS	Control Systems Engineering	20A04501	Dr. M.Ramprasad Reddy
2	DSP	Digital Signal Processing	20A04502T	Dr. R. Thriveni
3	MPMC	Microprocessors and microcontrollers	20A04503T	Mr. N. Kishore Kumar
4	CAO	Computer Architecture & Organization	20A04504a	Mrs. B. Vijitha
5	JP	Open Elective - I : Java Programming	20A05505a	Mr. G. Venu Babu
6	MPMC LAB	Microprocessors and Microcontrollers Lab	20A04503P	Mrs. B. Vijitha/Mr. B. R. Darshan
7	DSP LAB	Digital Signal Processing Lab	20A04502P	Dr. R. Thriveni/Mr. A. Amareswara Kumar
8	PCB LAB	Skill oriented course - III : PCB Design and Prototype development	20A04509	Mr. M. Karunakar Reddy/Mr. P. Jayaram Reddy
9	ES	Environmental Science	--	Ms. K. Venkata Lavanya
10	Activity	Activity		Mr. N. Kishore Kumar
Class In-charge:				Mrs. B. Vijitha

D. Darshan
Department Timetables Officer

Vijitha
Head of the Department
Head of the Department
Electronics & Communication Engineering
ADITYA COLLEGE OF ENGINEERING
MADANAPALLE - 517 325

K. Venkata Lavanya
Principal
Principal
Aditya College of Engineering
Madanapalle - 517325