



**ADITYA COLLEGE OF ENGINEERING: MADANAPALLE**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**TIME TABLE**



Academic Year :2023-2024 EVEN SEM

Year: III-II B. Tech -ECE -A

Room No:316

w.e.f: 22-01-2024

DAY/TIME	09:00 10:00	10:00 11:00	11:00 11:10	11.10 12.05	12:05 01:00	01:00 01:50	01:50 02:50	02.50 03.50	03.50 04.00	4.00 5.00
MON	X	AME	B R E A K	ESD	VLSI	L U N C H  B R E A K	IPR	POS	B R E A K	SEMINAR
TUE		ESD		VLSI	AME		← VLSI(A1)/AME(A2)			LAB→
WED		CN		VLSI	ESD		IPR	POS		AME
THU		AME		CN	POS		← VLSI(A2)/AME(A1)			LAB→
FRI		CN		POS	APTITUDE		← CN LAB			→
SAT		VLSI		AME	CN		ESD(T)	← RFSD		LAB→

S.No.	Title of the Subject		Subject Code	Name of the Faculty
1	AME	Antennas & Microwave Engineering	20A04601T	Dr. P. Gangadhara Reddy
2	VLSI	VLSI Design	20A04602T	Mr. C. Manoj Kumar
3	CN	Communication Networks	20A04603T	Mr. K. Balachandra
4	ESD	Professional Elective Course-II :Embedded System Design	20A04604b	Dr. M. Jagadeesh Babu
5	POS	Open elective II-Principles of Operating Systems	20A05605a	Mrs B. Vijitha
6	AME LAB	Antennas & Microwave Engineering Lab	20A04601P	Dr. P. Gangadhara Reddy/Mr. A. Amareswara Kumar
7	VLSI LAB	VLSI Design Lab	20A04602P	Mr. C. Manoj Kumar/ Dr. M. Jagadeesh Babu
8	CN LAB	Communication Networks Lab	20A04603P	Mr. K. Balachandra/ Mr. B.R. Darshan
9	RFSD LAB	Skill oriented course - IV :RF System Design	20A04607	Mr. M. Karunakar Reddy/Mr. A. AmareswaraKumar
10	IPR	Mandatory Non-credit Course :Intellectual Property Rights & Patents	20A99601	Mr. K. Manju Balaji
11	Aptitude	Aptitude		Mr. C. Manoj Kumar
12	Seminar	Seminar		Mr. K. Balachandra
Class In-charge				Mrs B. Vijitha

*Warshan*  
 Department Timetables Officer

*Vacc*  
 Head of the Department

*K. S. B. Bro*  
 Principal  
 Aditya College of Engineering  
 Madanapalle-517 325